

Amendments to the Specification

Kindly amend the specification as follows:

Page 1, between the title and the heading "**BACKGROUND OF THE INVENTION**", insert

--CROSS REFERENCE TO RELATED APPLICATIONS

This is a divisional application of application Serial No. 10/278,788, filed October 24, 2002, which is hereby incorporated by reference in its entirety for all purposes.—

Please amend the title as follows:

DRIVE CIRCUIT FOR DRIVING A CURRENT-DRIVEN DISPLAY UNIT

Please replace the paragraph beginning on page 3, line 7 with the following amended paragraph:

The cathode of the EL device EL12, i.e., the data line SEG2 is supplied with the ground potential GND by the switch means SWs2 of the data line drive circuit 1005. Since the anode of the EL device EL12 is not supplied with a current through the constant current device CC2, [[no]] current I1 does not flow through the EL device EL12 and hence the EL device EL12 is not caused to transition to the light emitting state.

Please replace the paragraph beginning on page 9, line 11 with the following amended paragraph:

The PMOS transistor P703, which serves as resistance means, is connected to the PMOS transistor P307. Described in detail, the PMOS transistor P703 has a source connected to the data line source potential Vs, a drain connected to the source of the PMOS transistor ~~[[P305]]~~ P307, and a gate connected to the ground potential GND. While the PMOS transistor P703 is normally kept in an ON state, it functions as a resistive element because the PMOS transistor P703 has a predetermined on resistance. The PMOS transistor P703 is also formed in the region 407 lying within the drive region 401.

Please replace the abstract with the following amended abstract:

A drive circuit includes an input node for receiving ~~[[data,]]~~ data and an output node. The drive circuit also includes a first MOS transistor of a first conductivity type and a second MOS transistor of the first conductivity type. The first MOS transistor has a source, a drain connected to the output node, and a gate connected to the input node. The second MOS transistor has a source, a drain connected to the source of the first MOS transistor, and a gate supplied with a predetermined potential level. The drive circuit also includes a resistance ~~[[means]]~~ connected between the source of the second MOS transistor and a source node supplied with a source potential level.